

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Koichi OHTO
Appl. No.: 09/727,675 Group: 2812
Filed: December 4, 2000 Examiner: UNASSIGNED
For: METHOD FOR FABRICATING SEMICONDUCTOR
DEVICE AND APPARATUS FOR FABRICATING
SAME

COPY

LETTER

Assistant Commissioner for Patents
Washington, DC 20231

Date: June 14, 2001

Sir:

Under the provisions of 35 U.S.C. § 119 and 37 C.F.R. § 1.55(a), the applicant(s) hereby claim(s) the right of priority based on the following application(s):

<u>Country</u>	<u>Application No.</u>	<u>Filed</u>
JAPAN	11-343510	December 2, 1999


A certified copy of the above-noted application(s) is(are) attached hereto.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 25-0120 for any additional fee required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

YOUNG & THOMPSON

By


Robert J. Patch, #17,355

745 South 23rd Street, Suite 200
Arlington, Virginia 22202
(703) 521-2297

Attachment

日 本 国 特 許 庁

PATENT OFFICE
JAPANESE GOVERNMENT

別紙添付の書類に記載されている事項は下記の出願書類に記載されて
る事項と同一であることを証明する。

This is to certify that the annexed is a true copy of the following application as filed
in this Office.

出 願 年 月 日

Date of Application:

1999年12月 2日

願 番 号

Application Number:

平成11年特許願第343510号

願 人

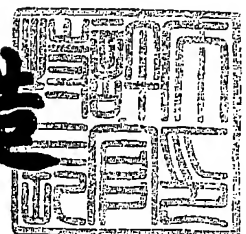
Applicant(s):

日本電気株式会社

2000年 9月 1日

特許庁長官
Commissioner,
Patent Office

及 川 耕 造



出証番号 出証特2000-3069511